

PATENT ABSTRACTS OF JAPAN

(11)Publication number : 02-307145

(43)Date of publication of application : 20.12.1990

(51)Int.Cl. G06F 12/10
G06F 9/46
G06F 9/46

(21)Application number : 01-128087 (71)Applicant : NIPPON TELEGR &
TELEPH CORP <NTT>

(22)Date of filing : 22.05.1989 (72)Inventor : SHIOKAWA SHIZUO
KOBASHI YOSHITSUGU

(54) VIRTUAL COMPUTER SYSTEM

(57)Abstract:

PURPOSE: To increase the overall processing speed of a virtual computer system by saving temporarily only the entry of a preferential OS into a high speed memory contained in a CPU and transferring the entry to an address conversion buffer from the memory at the OS dispatch when the preferential OS runs again.

CONSTITUTION: When a preferential OS decided previously is switched the information on an address conversion buffer can be saved. In a figure (a) for example the contents of the address conversion buffer of an address conversion buffer system circuit 1 are saved into a saving memory 100 in parallel with the saving and the recovery of the contents of a register. In a figure (b) the address information is recovered to the address conversion buffer of the circuit 1 from the

memory 100 in parallel with the saving and the storage of the contents of the register. Thus these operations can be carried out in parallel with the register saving/recovery operations. Therefore the influence of the address conversion information itself is practically reduced to cause the deterioration of performance when the conversion information is saved into the memory 100. As a result the overall processing speed is improved for a virtual computer system.